

ABSTRACT

An in-plane switching mode liquid crystal display device is disclosed which comprises first and second substrates, a plurality of gate and data bus lines formed on said first substrate to define a plurality of pixel regions, a common bus line aligned in each pixel regions of the first substrate, a thin film transistor(TFT) formed at each pixel regions of the first substrate, a data electrode which is formed on a gate insulator of the TFT and has a portion overlapping the common bus line for forming a first storage capacitor, a passivation layer formed over the data electrode and the TFT, a common electrode which is formed on the passivation layer so as to overlap the gate and data bus lines and has a portion overlapping said data electrode for forming a second storage capacitor, and a liquid crystal layer formed between the first and second substrates.